

Electronic Filing System (EFS) Data

Electronic Patent Application Submission

USPTO Use Only

EFS ID: 56949
Application ID: 10708563 

Title of Invention: STRUCTURE AND METHOD OF
FORMING A BIPOLAR
TRANSISTOR HAVING A VOID
BETWEEN EMITTER AND
EXTRINSIC BASE

First Named Inventor: Rama Divakaruni
Domestic/Foreign Application: Domestic Application
Filing Date: 2004-03-11
Effective Receipt Date: 2004-03-11
Submission Type: Utility Patent Filing
Filing Type: new-utility
Confirmation number: 2562
Attorney Docket Number: FIS920030414US1

Total Fees Authorized: 810.0
Payment Category: Deposit Account
Deposit Account Number: 90458
Deposit Account Name: H. Daniel Schnurmann
RAM Payment Status: RAM has not been processed

Digital Certificate Holder: cn=H. Daniel Schnurmann,ou=Registered Attorneys,ou=Patent and
Trademark Office,ou=Department of Commerce,o=U.S. Government,c=US
Certificate Message Digest: f3db9cf44a581c7ebe666d54b94ba90d0d1737a0